

Roll No. 

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**Total No. of Pages: 01**  
**Total No. of Questions: 07****BCA (Sem.-3<sup>rd</sup>)**  
**COMPUTER SYSTEM ARCHITECTURE****Subject Code: BC-403****Paper ID: [B0226]****Time: 3 Hrs.****Max. Marks: 60****INSTRUCTIONS TO CANDIDATE:**

1. *Section –A, is Compulsory.*
2. *Attempt any four questions from Section-B.*

**Section –A****(10x2=20)**

- Q.1.(a) Define instruction codes?
- (b) Define MAR?
- (c) Name some RR instructions?
- (d) What is Branch instruction?
- (e) Define hit ratio?
- (f) What is cache memory?
- (g) Define EPROM?
- (h) Give example of three address instruction?
- (i) Define I/O instructions?
- (j) Define Auxiliary Memory?

**Section –B****(4x10=40)**

- Q.2. How timing and control signals are generated by CPU?
- Q.3. Explain the working of instruction cycle?
- Q.4. Discuss various types of memories?
- Q.5. Explain stack in CPU organization?
- Q.6. Discuss various types of addressing formats?
- Q.7. Explain the working of cache memory?